# SONAL BHAT MS ELECTRONICS ENGINEERING Spring 2005

Temporary Student No: 321-234132

#### **RESUME**

**NAME**: SONAL BHAT

<u>OBJECTIVE</u>: To acquire a MS degree leading to a PhD in Powertronics. I wish to implement the knowledge, I gain through this program in developing new technologies which will make generation, transmission and distribution of electrical energy more cost effective i.e. Energy Saving.

# **ACADEMIC ACHIEVEMENTS:**

- 1) 1<sup>st</sup> Rank in Mumbai university (Electrical Engineering)
- 2) Kulapati's Gold Medal for the academic year 2000-2001
- 3) Shri Pruthvish Narayandas Killawala Prize for the academic year 1999-2000
- 4) First Merit Prize for the academic year 1999-2000
- 5) Merit Prize (3rd Rank) for the academic year 1998-1999
- 6) Jadhavpur University Alumni Association Trust Scholarship (academic year 1999-2000)
- 7) Bengal engineering College Alumni Association Trust Scholarship (academic year 1999- 2000)
- 8) Ratan Tata Scholarship
- 9) Science Talent Search Certificate awarded by Greater Bombay Teachers Association
- 10) Senior General Knowledge Test Certificate awarded by UnitedSchools Organizationsof India

# **ACADEMIC RECORDS:**

| Exam Passed        | School / College                            | Year    | % Of<br>Marks | Rank                                 |
|--------------------|---|---------|---------------|--------------------------------------|
| B.E. VIII Semester | Sardar Patel College Of Engineering         | June'01 | 80.00%        | -1st in Mumbai University            |
| B.E. VII Semester  | Sardar Patel College Of Engineering         | Dec'00  | 69.65%        |                                      |
| B.E. VI Semester   | Sardar Patel College Of Engineering         | June'00 | 74.40%        |                                      |
| B.E. V Semester    | Sardar Patel College Of Engineering         | Dec'99  | 69.93%        |                                      |
| B.E. IV Semester   | Sardar Patel College Of Engineering         | May'99  | 73.60%        | 3rd rank in Electrical Department    |
| B.E. III Semester  | Sardar Patel College Of Engineering         | Dec'98  | 66.48%        |                                      |
| B.E. II Semester   | Sardar Patel College Of Engineering         | May'98  | 61.80%        | 5th rank in Electrical<br>Department |
| B.E. I Semester    | Sardar Patel College Of Engineering         | Dec'97  | 60.50%        |                                      |
| Higher Secondary   | SIES College Of Arts, Science And Commerce. | Mar'97  | 85.17%        | -                                    |
| Secondary School   | Auxilium Convent High School                | Mar'95  | 87.71%        | -                                    |

SCORES: GRE – 2030 (Quantitative – 760, Analytical - 750, Verbal - 520)

TOEFL - 290

# **ACADEMIC PROJECT:**

Title: Optimal Location of Capacitors For Power Factor Improvement

Duration: July 2000 - March 2001

Company: BSES Ltd. (A multiutility company in the field of power generation, transmission and

distribution.)

**Aim:** To study the importance of power factor improvement in power systems and find out the KVAR requirements and the optimal location of the capacitors to improve the power factor to the

desired value.

Synopsis: Attached along with.

#### **WORK EXPERIENCE:**

**Post:** Executive.

**Duration:** 20 th August 2001 till date.

Company: Siemens Ltd.

**Division:** Automation and Drives.

#### **Nature of Duties:**

In depth study of all switchgear products like Air Circuit Breakers, Moulded case Circuit Breakers, Contactors, Relays, Motor protection Circuit Breakers, Starters etc manufactured by both Siemens Ltd. as well as its major competitors like GE, ABB Ltd, Schneider Electric Ltd. etc Development of new Switchgear products in association with the design colleagues. I was closely associated with the development of a new range of capacitor duty contactors.

Actively involved in the study of communication capable switchgears- a recent trend in the field of low voltage switchgear.

Carry out mechanical and electrical testing of switchgear products in the manufacturing stage. Studying different applications in different industries and suggesting switchgear products most suitable for these applications.

Provide technical support required for selling the switchgear items manufactured by Siemens Ltd.

# **LIST OF ARTICLES PUBLISHED:**

| Name Of The Article                                  | Name Of The Magazine          |  |  |
|--|-------------------------------|--|--|
| 1. MPCB with Relay Function                          | 1. Electrical India (May 02)  |  |  |
| 11. WE'CD WILL Relay Fullction                       | 2. Industry 2.0 (May 02)      |  |  |
| 2.Switching Capacitors For Power Factor              | 1.Electrical Review (Sept 02) |  |  |
| Improvement  | 2. Electrical India (Aug 02)  |  |  |
| 3. Emerging Trends in Low Voltage Switchgear Systems | 1.Electrical Review (Sept 02) |  |  |

# **EXTRA CURRICULAR ACTIVITIES:**

Organizer for "Callipainting"- painting cum calligraphy competition in Technobreak '99 - an inter class festival of Sardar Patel College Of Engineering.

Member of the reception committee in SPACE' 99 - an intercollegiate festival organized by Sardar Patel College of Engineering each year. More than 100 colleges from all over Maharashtra participate in SPACE each year.

# **PERSONAL DETAILS:**

Name: Sonal Bhat Gender: Female.

Date of Birth: 13th MAY 1979.

Marital Status: Single.

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Kurla (e) - Mumbai, Maharashtra State

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Languages known: English, Hindi, French, Bengali and Marathi.